



| REVISION HISTORY |                      |                |          |
|------------------|----------------------|----------------|----------|
| REV              | DESCRIPTION          | DATE           | APPROVED |
| R1-D1            | initial release      | August 10-2023 | GDV      |
| R1-D2            | updated pinout table | March 13-2024  | GDV      |

| TERMINAL BLOCK PINOUT |               |
|-----------------------|---------------|
| PIN                   | SIGNAL        |
| 1                     | POWER -       |
| 2                     | POWER+        |
| 3                     | SOLENOID -    |
| 4                     | SOLENOID +    |
| 5                     | INPUT +       |
| 6                     | INPUT GND     |
| 7                     | AUX OUTPUT    |
| 8                     | +5V REFERENCE |



|   |  |   |  |  |   |  |  |
|---|--|---|--|--|---|--|--|
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| <small>DIMENSIONS ARE IN mm UNLESS OTHERWISE SPECIFIED IN DRAWING TOLERANCE ON:</small><br>X.X ± 0.5<br>X. ± 1<br>ANGLES ± 1.0  |  | <small>MATERIAL</small><br>NA<br><small>FINISH</small><br>NA<br><small>THIRD ANGLE PROJECTION</small><br> | <small>DRAWN</small><br>TF<br><small>CHECKED</small><br>TF<br><small>INVENTOR</small><br>GDV<br><small>TOOL</small><br>B<br><small>SIZE</small><br>B | <small>DATE</small><br>2023-08-10<br><small>DATE</small><br><br><small>DATE</small><br>  | <small>TITLE</small><br>Universal Input, Single Output 5A Valve Controller, NFC, in housing with acrylic cover<br><small>DWG REV</small><br>D2<br><small>SCALE</small><br>1.5 | <small>DWG NO</small><br>AX020720-PG9-MD<br><small>PART NO</small><br>AX020720-PG9<br><small>SHEET</small><br>1 of 1 |  |